

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
John L. Fulton, et al.)	Art Unit: 1792
Serial No: 10/783,249)	Examiner: Nicole R. Blan
Filed: 2/19/04)	Confirmation No.: 1416
For: Process and Apparatus for)	File No: 14138-B
Removing Residues from Semiconductor)	Date: April 21, 2008
Substrates)	

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Mail Stop Amendment
Commissioner for Patents
P.O. BOX 1450
Alexandria, Virginia 22313-1450

Dear Sir:

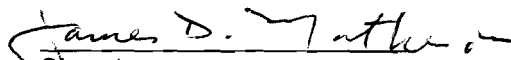
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☒ Supplemental Information Disclosure Statement, Form PTO/SB/08A with
References

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